May-27-03 03:06pm From- T-282 P.003/005 F-259

In re: Hans Jurgen Mattausch

Appl. No.: 09/500,254 Filed: February 8, 2000

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II. The Cited Combination of References is Improper

Applicant notes that all of the claims are rejected based on a combination of the '570 Huegel patent with the '674 DeWilde patent. Applicant respectfully submits, however, that the combination cited in the Office Action against the claims is improper, as the '674 DeWilde patent is not considered prior art to the claimed invention under any section of 35 U.S.C. § 102. Specifically, the '674 DeWilde patent has a filing date of April 4, 2000. The present application was filed February 8, 2000 in the US and February 12, 1999 in Japan. As such, the present application predates the filing date of the '674 DeWilde patent. The '674 DeWilde patent is therefore not prior art to the present application and cannot be used to reject the claims of the application.

As conceded in the Office Action, the '570 Huegel patent does not teach or suggest multiport memories that provide internally concurrent access. As the '674 DeWilde patent is not prior art, and this aspect is not taught or suggested by any of the other cited prior art references, Applicant respectfully submits that independent Claim 1, as well as the claims that depend therefrom, is patentable over the cited references.

III. The '570 Huegel Patent Does Not Teach or Suggest Copy Busses

Applicant agrees with the Office Action's conclusion that the '570 Huegel patent does not teach or suggest internal concurrent access. Applicant, however, respectfully disagrees with the Office Action's conclusion that the '570 Huegel patent teaches or suggests copy busses as is alleged in paragraph 6 of the Office Action. Specifically, while the structure disclosed in Fig. 1 and 2 of the '570 Huegel patent contains a multiport memory and busses, the busses do not act as copy busses. The '570 Huegel patent nowhere discloses use of the busses as copy busses between the 3-port SRAMs. Instead, these busses are used only as access-busses to the SRAMs for the processors. (See Figs 1,2; abstract lines 12-18, column 4 lines 14-35, column 5 lines 12-40). Applicant thus submits that the '570 Huegel patent does not teach or suggest either 1) copy busses or 2) concurrent access as is recited in independent Claim 1 of the present application.

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CONCLUSION

In view of the remarks presented above, it is respectfully submitted that all of the present claims of the application are in condition for immediate allowance. It is therefore respectfully requested that a Notice of Allowance be issued. The Examiner is encouraged to contact Applicant's undersigned attorney to resolve any remaining issues in order to expedite examination of the present application.

It is not believed that extensions of time or fees for net addition of claims are required, beyond those that may otherwise be provided for in documents accompanying this paper. However, in the event that additional extensions of time are necessary to allow consideration of this paper, such extensions are hereby petitioned under 37 CFR § 1.136(a), and any fee required therefore (including fees for net addition of claims) is hereby authorized to be charged to Deposit Account No. 16-0605.

Respectfully submitted,

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May-27-03 03:07pm From- T-282 P.005/005 F-259

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Version with Markings to Show Changes Made:

In the Claims:

Please cancel Claim 6.

CLT01/4593501v1